

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S3	50	((((711/103).CCLS.) and @ad < "20010830") and flash) and compression	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 11:57
S2	600	((711/103).CCLS.) and @ad < "20010830") and flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 11:57
S4	16	(((((711/103).CCLS.) and @ad < "20010830") and flash) and compression) and decompress\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 16:10
S6	3	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (synchronous with RAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 16:12
S5	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (synchronous near2 RAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 16:12
S7	4	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and ((synchronous with RAM) sdram sldram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 16:13
S8	97	(flash with memory with compress\$4) and ((power adj up) boot) and processor and @ad< "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 16:59
S9	75	(flash with memory with decompress\$4) and processor and @ad< "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 17:12
S10	2	"20030046483"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 17:13

S11	2	"20030046486"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 17:14
S12	2	"6549468"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 17:15
S14	2	"20030023840"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 17:20
S13	7	"6560161"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/30 17:20
S15	2209	(flash with memory) and (power adj up) and processor and @ad< "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/03 13:38
S17	34	((flash with memory) with (RAM) with (load read)) same ((power adj up) reset)) and processor and @ad< "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/03 15:46
S18	184	((flash with memory) with (RAM) with (load\$3 read\$3 stor\$3 cop\$4)) same ((power adj up) reset\$4 boot\$4)) and processor and @ad< "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/03 15:47
S19	152	((flash near4 memory) with (RAM) with (load\$3 read\$3 stor\$3 cop\$4)) same ((power adj up) reset\$4 boot\$4)) and processor and @ad< "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/03 16:13
S20	2	"20020138702"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/03 16:18
S1	1057	(711/103).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/05/04 18:17

S21	2	"20020016888"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/04 18:22
S22	2	"20020016888" and (exclusive adj lock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/04 18:23
S23	2	"20030046485"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/12 21:16
S25	232	((CPU processor system) adj reset) with (boot)) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/12 21:18
S24	338	((CPU processor system) adj reset) with (boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/12 21:22
S26	3	((CPU processor system) adj reset) with (boot near5 (preload (pre adj load))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 13:33
S27	15	((boot near5 load) near (complet\$4 ack acknowledg\$5)) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 13:41
S28	9	((boot near5 load) with (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 13:43
S29	2	(((boot near5 load\$4) near4 (complet\$4 after)) with (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:27
S31	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and RDRAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:28

S30	65	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:28
S33	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (Rambus with RAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:29
S32	0	((flash with memory with compress\$4) and (power adj up) and processor and @ad< "20010830") and (Rambus with DRAM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:29
S35	4	((flash with memory) and (power adj up) and processor and @ad< "20010830") and (synchronous with ram)and rambus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:30
S16	36	((flash with memory) and (power adj up) and processor and @ad< "20010830") and (synchronous with ram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:30
S34	2566	(Rambus with (RAM.dram.rdram))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:31
S36	1504	((Rambus with (RAM dram rdram))) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:33
S38	3	((711/103).CCLS.) and @ad < "20010830") and rambus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:34
S37	1	((711/103).CCLS.) and @ad < "20010830") and rdram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/13 18:34
S39	0	((flash near3 eprom) with serial) and RDRAM and @ad < "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 14:05

S40	4	((flash near3 eprom) with serial) and @ad < "20010830") and (flash with boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 14:06
S42	0	((flash near3 (serial adj bus)) with boot) and @ad < "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 14:37
S45	29	((flash near5 (serial adj bus))) and @ad < "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 14:38
S44	0	((flash near5 (serial adj bus)) same boot) and @ad < "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 14:38
S43	1	((flash near3 (serial)) with boot) and @ad < "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 14:38
S41	112	((flash near3 eprom) with serial) and @ad < "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 16:15
S49	164	(((713/2).CCLS.) and @ad < "20010830") and flash) and serial	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 16:16
S48	369	(((713/2).CCLS.) and @ad < "20010830") and flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 16:16
S47	236074	(((713/2).CCLS.) and @ad < "20010830") flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/05/14 16:16
S46	1008	(713/2).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/05/14 16:16

S50	2	("20030046486").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/31 17:22
S54	3	(system adj reset) with (cpu.near4 activa\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/31 19:23
S53	17	((((713/2).CCLS.) and @ad < "20010830") and flash) and serial) and SDRAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/31 19:23
S51	859	((711/103).CCLS.) and @ad < "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/31 19:23
S55	15	((CPU processor system) adj reset) near signal) with (boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/31 19:24
S57	0	((boot near5 load) near (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/31 19:26
S56	370	((713/2).CCLS.) and @ad < "20010830") and reset and boot	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/31 19:26
S52	942	((713/2).CCLS.) and @ad < "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/10/31 19:26
S58	2	"6058474".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 15:37
S59	426	((713/2).CCLS.) and @ad < "20010830") and flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 21:37

S61	241	((CPU processor system) adj reset) with (boot)) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 21:38
S60	647	((711/103).CCLS.) and @ad < "20010830") and flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 21:38
S63	15	((CPU processor system) adj reset) near signal) with (boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 21:39
S62	18	(((713/2).CCLS.) and @ad < "20010830") and flash) and serial) and SDRAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 21:39
S64	377	(((713/2).CCLS.) and @ad < "20010830") and reset and boot	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 21:40
S67	21	((((713/2).CCLS.) and @ad < "20010830") and flash) and serial) and SDRAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 16:42
S66	439	(((713/2).CCLS.) and @ad < "20010830") and flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 16:42
S65	658	((711/103).CCLS.) and @ad < "20010830") and flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 16:42
S68	387	(((713/2).CCLS.) and @ad < "20010830") and reset and boot	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 16:43
S71	10	((boot near5 load) with (assert\$4 deassert\$ de-assert\$4)) and @ad < "20010828"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 16:44

S70	84	(flash with memory with decompress\$4) and processor and @ad< "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 16:44
S69	15	((CPU processor system) adj reset) near signal) with (boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/07 16:44
L1	1494	(711/103).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/10 15:21
L3	662	((711/103).CCLS.) and @ad < "20010830") and flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/10 15:22
L2	897	((711/103).CCLS.) and @ad < "20010830"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/10 15:22
L6	18	((CPU processor system) adj reset) near signal) with (boot)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/10 15:23
L5	397	((713/2).CCLS.) and @ad < "20010830") and reset and boot	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/10 15:23
L4	39	((flash with memory) and (power adj up) and processor and @ad< "20010830") and (synchronous with ram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/10 15:23
L8	47	zitlaw.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/10 15:24
L7	22	(((713/2).CCLS.) and @ad < "20010830") and flash) and serial) and SDRAM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/10 15:24